

hardware interface connector having between 1 and 120 receptacles, the hardware interface port having 120 pins;

*C1
para 1d*

said receptacles configured to correspond to a pin in the hardware interface port, said receptacles further forming two rows in parallel such that each receptacle is positioned to be numbered corresponding to its position in one of the rows, wherein one of the two rows includes receptacle positions 1 through 60, the other of the two rows includes receptacle positions 61 through 120, receptacle positions 1 and 61 being located at corresponding ends of each row, receptacle positions 60 and 120 being located at corresponding ends of each row, receptacle positions 1 and 120 being located at opposite ends of each row, and receptacle positions 60 and 61 being located at opposite ends of each row.
